

Eötvös Loránd University

# Programmable Networks Lecture 3 – Stateful applications

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- Stateful programming
  - How to store state information?
- Fast reroute an example application
- Probabilistic data structures I
  - Bloom filters



## Stateful programming

### stateless vs stateful



stateless objects

stateful objects

reinitialized for each packet variables headers

keep state between packets tables registers counters meters

...

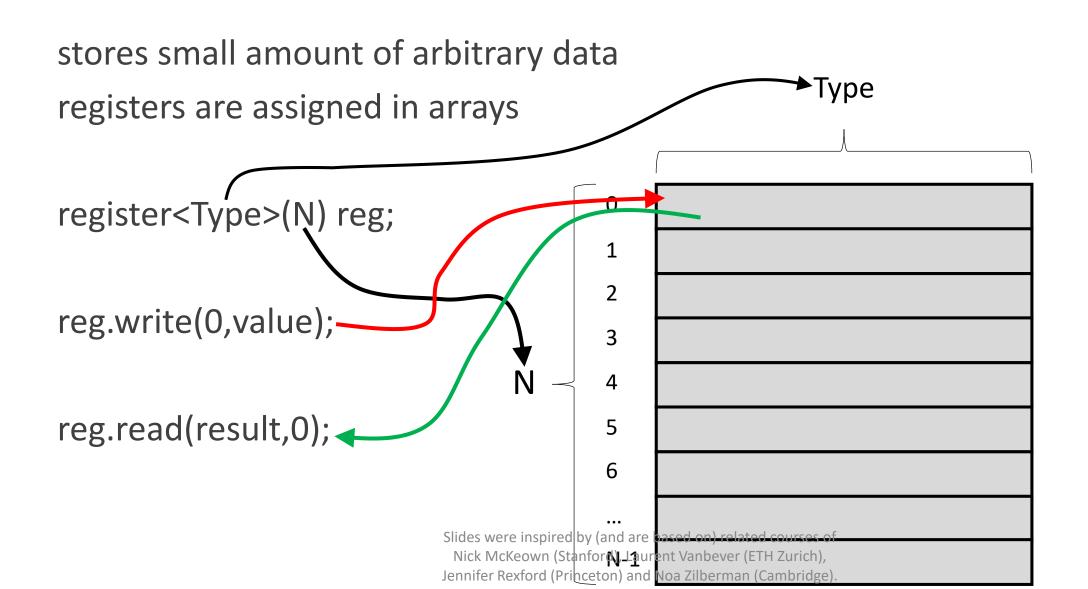
### stateful objects



| tables                              | managed by the control plane  |
|-------------------------------------|---|
| <b>register</b> (extern in v1model) | store arbitrary data can be managed by both data and control planes |
| <b>COUNTER</b> (extern in v1model)  | count events<br>like number of table entry matches                  |
| meter (extern in v1model)           | assign "colors" to packets<br>rate-limiting                         |

register





### register – calculating inter packet gap



register<bit<48>>(16384) last\_seen;

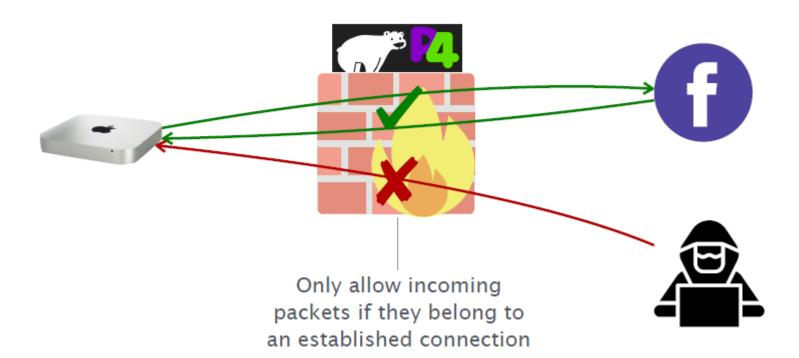
. . .

action get\_inter\_packet\_gap(out bit<48> interval, bit<32> flow\_id)

bit<48> last\_pkt\_ts; /\* Get the time the previous packet was seen \*/ last\_seen.read(last\_pkt\_ts, flow\_id); /\* Calculate the time interval \*/ interval = standard\_metadata.ingress\_global\_timestamp - last\_pkt\_ts; /\* Update the register with the new timestamp \*/ last\_seen.write(flow\_id, standard\_metadata.ingress\_global\_timestamp);

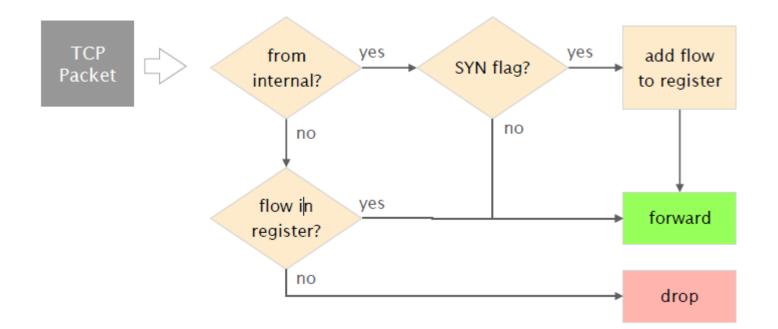
### example: stateful firewall





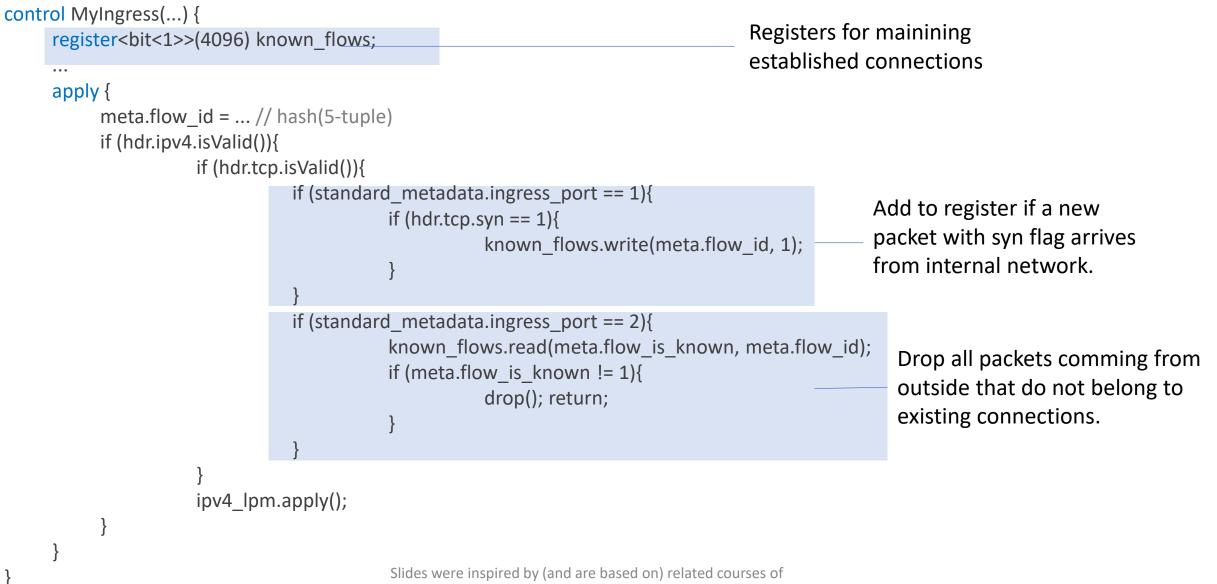
### stateful firewall





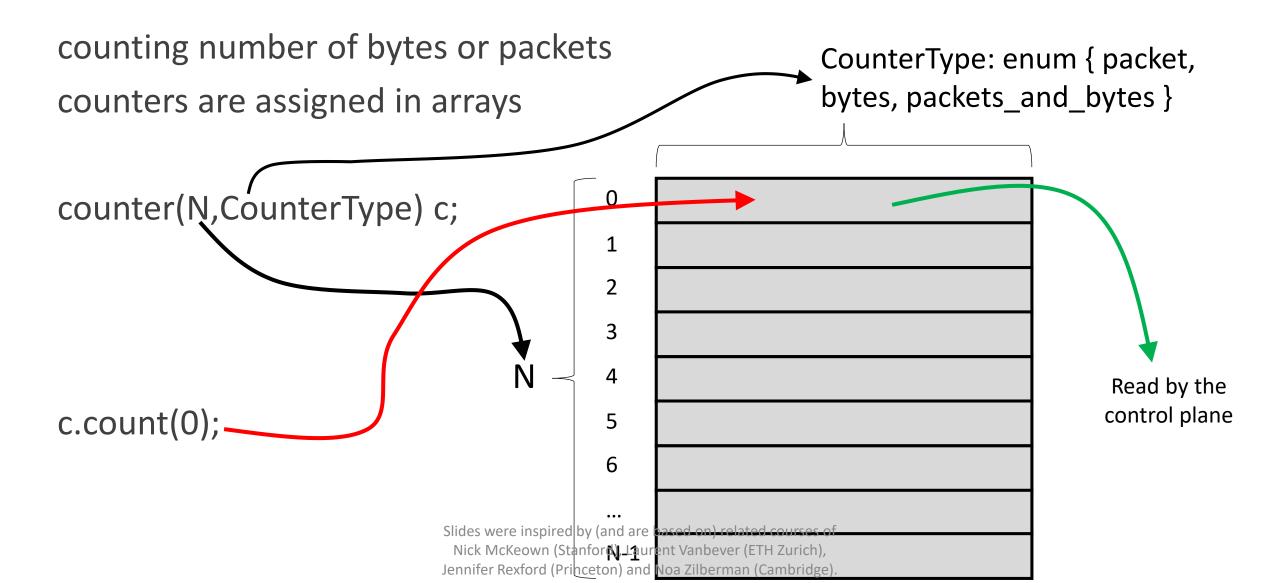
## stateful firewall





Nick McKeown (Stanford), Laurent Vanbever (ETH Zurich), Jennifer Rexford (Princeton) and Noa Zilberman (Cambridge). counter





example - port statistics ingress port is used as counter idx



```
control MyIngress(...) {
```

counter(512, CounterType.packets\_and\_bytes) port\_counter;

apply {
 port\_counter.count((bit<32>) standard\_metadata.ingress\_port);



### reading counter values from the control plane



RuntimeCmd: counter\_read MyIngress.port\_counter 1 MyIngress.port\_counter[1]= BmCounterValue(packets=13, bytes=1150)

# control My Ingress(...) { counter(512, CounterType.packets\_and\_bytes) port\_counter;

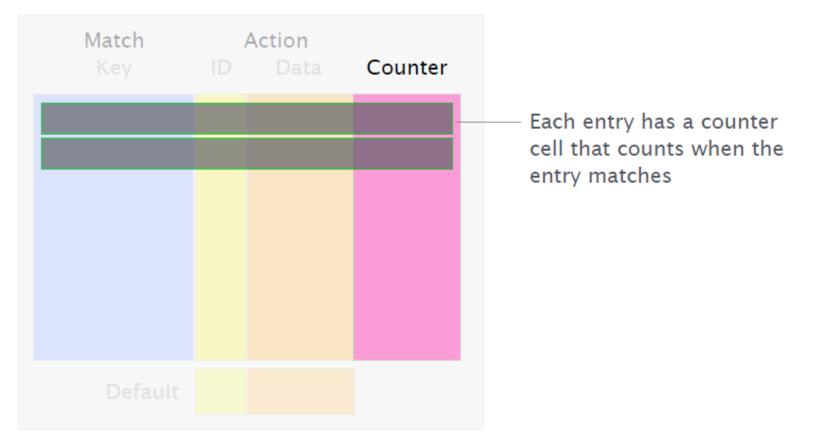
#### apply {

port\_counter.count((bit<32>) standard\_metadata.ingress\_port);

### direct counters



#### special counters attached to tables



### port statistics in a bit different way



control MyIngress(...) {

direct\_counter(CounterType.packets\_and\_bytes) direct\_port\_counter;

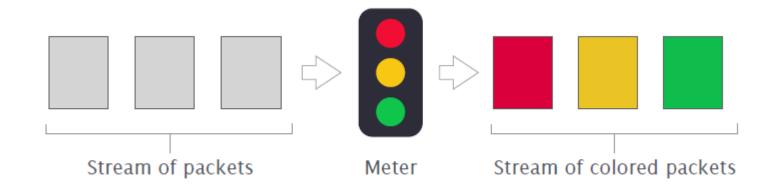
```
table count table {
        key = \{
                 standard_metadata.ingress_port: exact;
        actions = {
                 NoAction;
        default_action = NoAction;
                                                                        Attach counter
        counters = direct_port_counter;
                                                                        to the table
        size = 512;
```

apply {

```
count_table.apply();
```

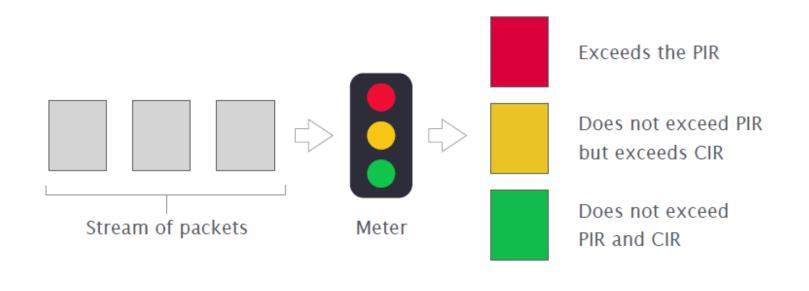
### meters





### meters



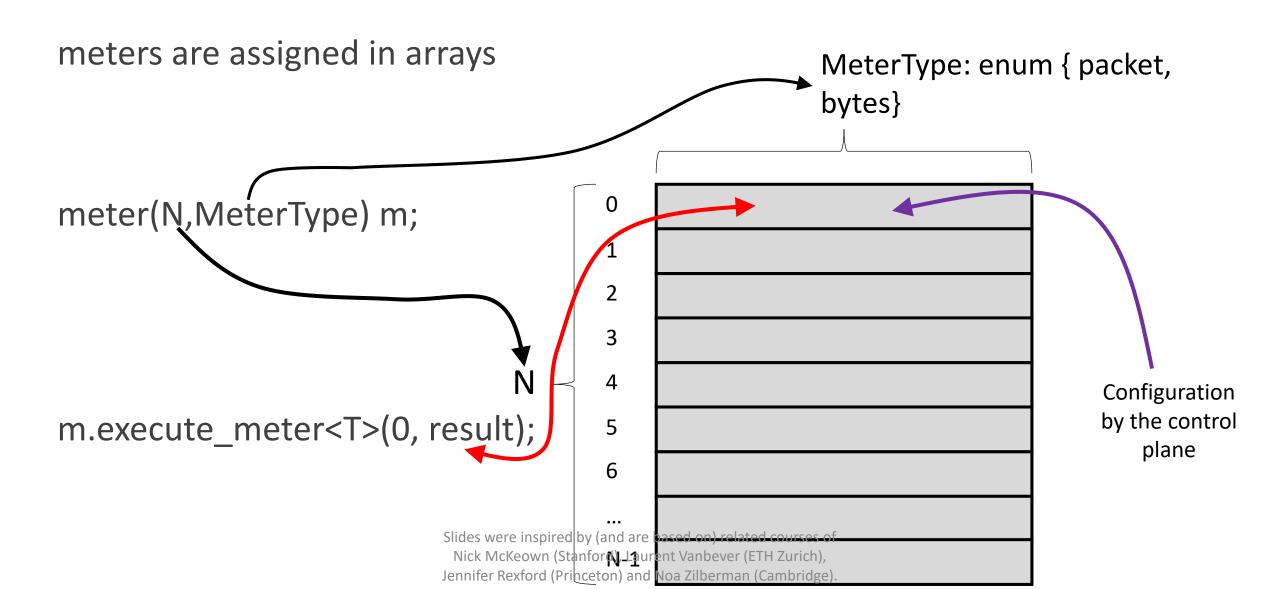


| Parameters: | PIR | Peak Information Rate      | [bytes/s] or [packets/s] |
|-------------|-----|----------------------------|--------------------------|
|             | CIR | Committed Information Rate | [bytes/s] or [packets/s] |

more info https://tools.ietf.org/html/rfc2698

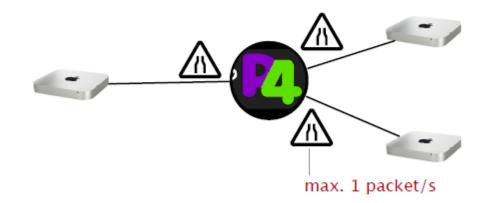
meter





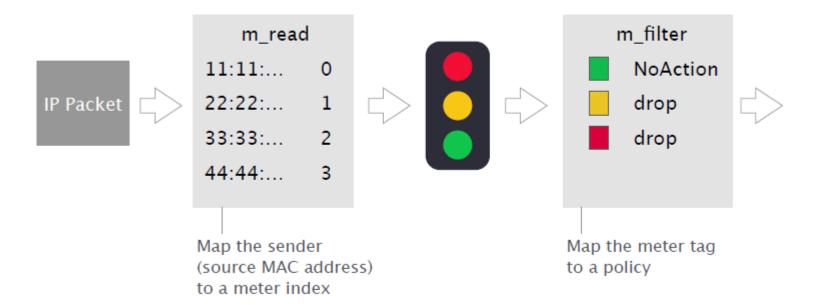
### example: rate-limiter





### example: rate-limiter





control MyIngress(...) {

meter(32w16384, MeterType.packets) my\_meter;



action m\_action(bit<32> meter\_index) {
 my\_meter.execute\_meter<bit<32>>(meter\_index, meta.meter\_tag);

```
table m_read {
    key = { hdr.ethernet.srcAddr: exact; }
    actions = { m_action; NoAction; }
    ...
```

```
execute meter &
store the color
in metafield
meter_tag
```

packet meter

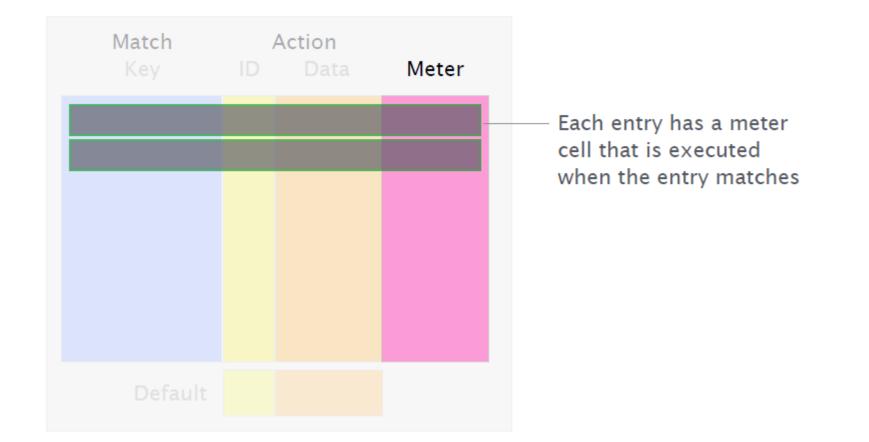
```
table m_filter {
    key = { meta.meter_tag: exact; }
    actions = { drop; NoAction; }
    ...
}
apply {
    m_read.apply();
    m_filter.apply();
}

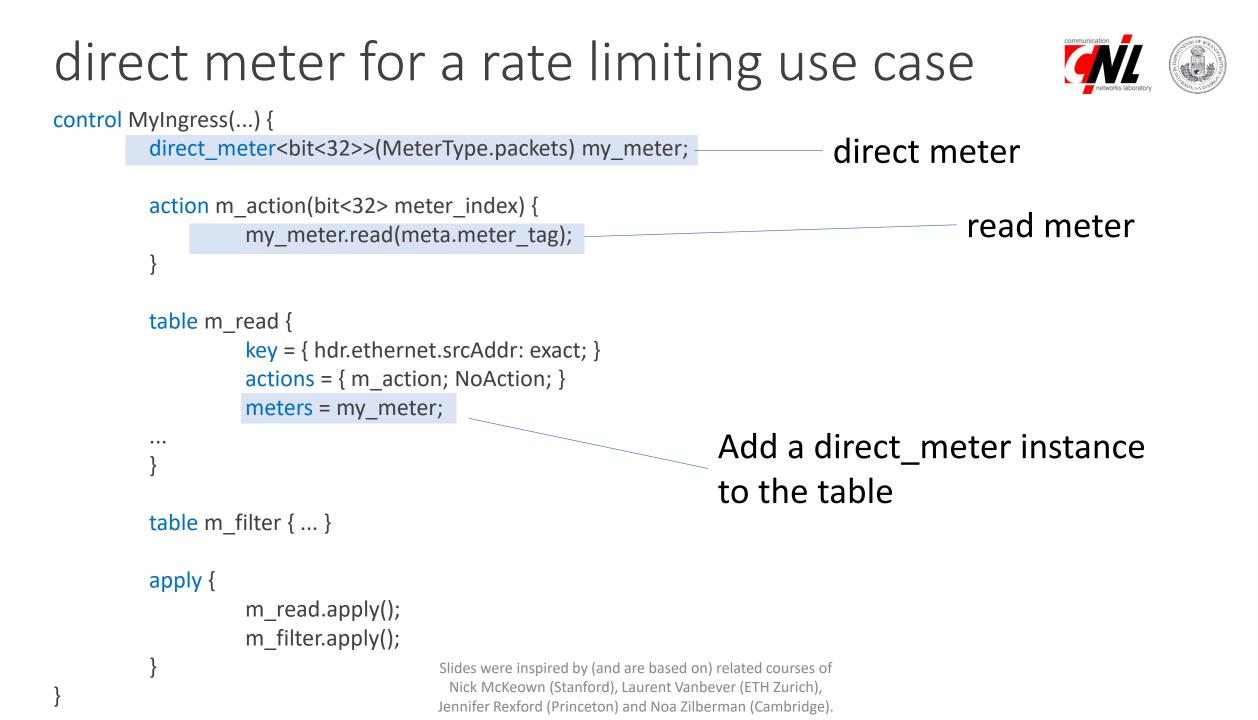
Slides were inspired by (and are based on) related courses of
Nick McKeown (Stanford), Laurent Vanbever (ETH Zurich),
```

Jennifer Rexford (Princeton) and Noa Zilberman (Cambridge).

### direct meters assigned to tables







### stateful summary



|          | data plane   |  |          | control plane |                    |
|----------|--------------|--|----------|---------------|--------------------|
|          | read         | write/modify                                     |          | read          | write/modify       |
| table    | apply()      | no   |          | yes           | yes                |
| register | yes – read() | yes – write()                                    |          | yes           | yes                |
| counter  | no           | yes – count()                                    |          | yes           | reset only         |
| meter    | yes          | <b>Yes</b><br>Slides were inspired by (and are l | based on | <b>NO</b>     | configuration only |



## An example application

https://www.net.t-labs.tu-berlin.de/~stefan/neat18.pdf

https://www.youtube.com/watch?v=G4L2ys- W9w#t=26m26s https://p4.org/assets/P4WS 2018/Marco Chiesa.pdf



## Probabilistic data structures I.

Bloom filters

### programming advanced data structs

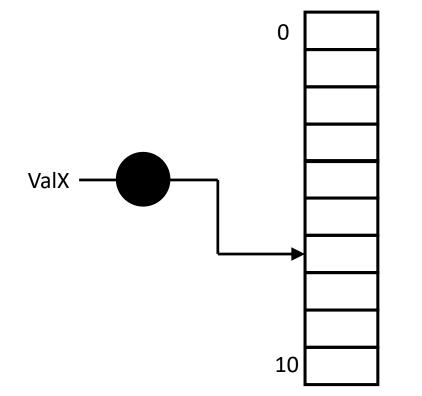


building blocks

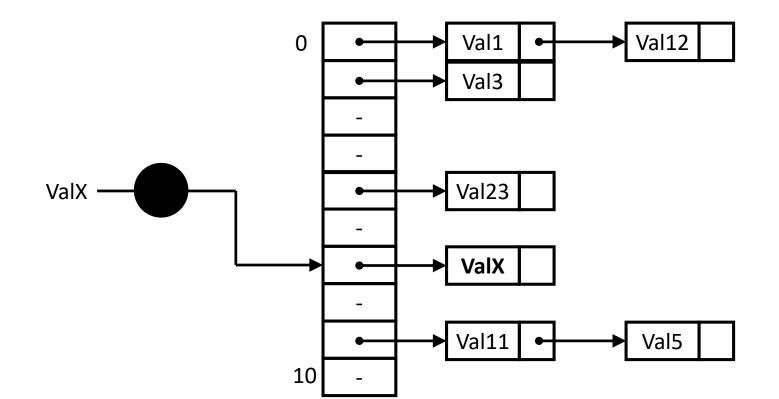
#### **built-in stateful data structures** arrays of registers, counters or meters

**lots of limitations** limited number of operations and memory

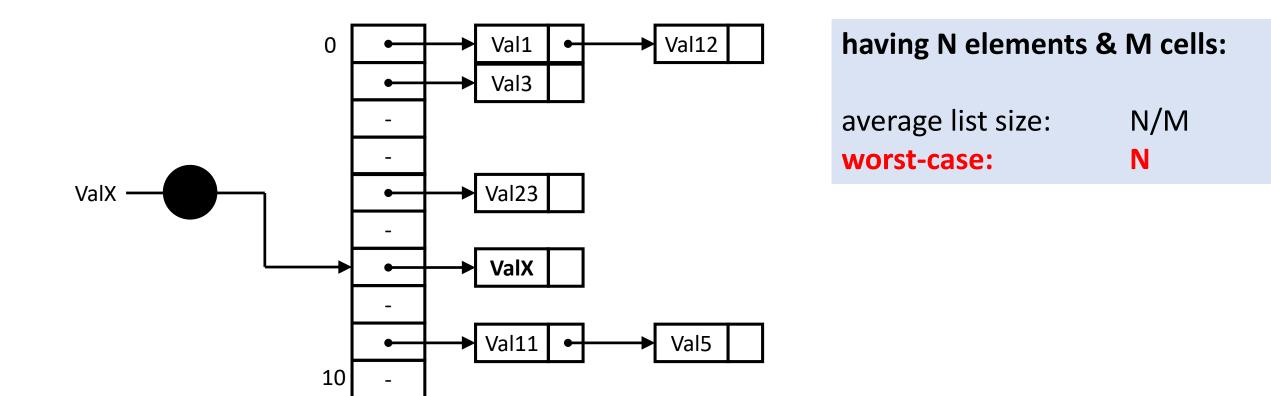




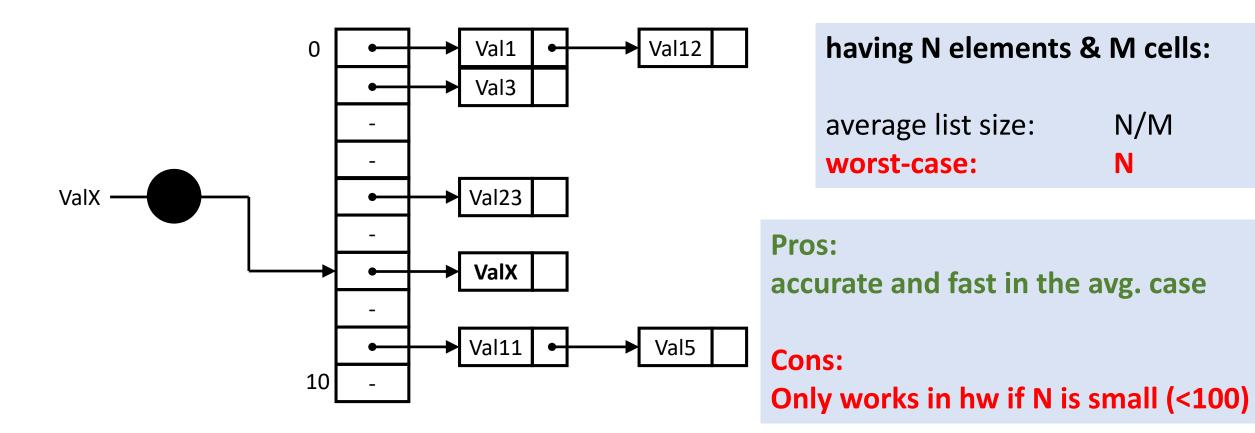




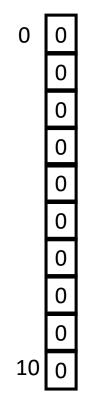






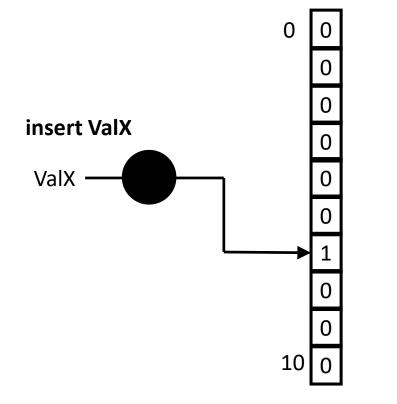






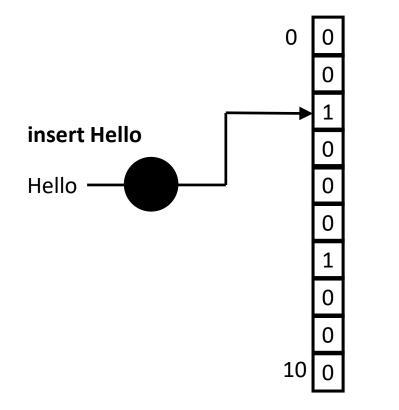
#### 1-bit cells





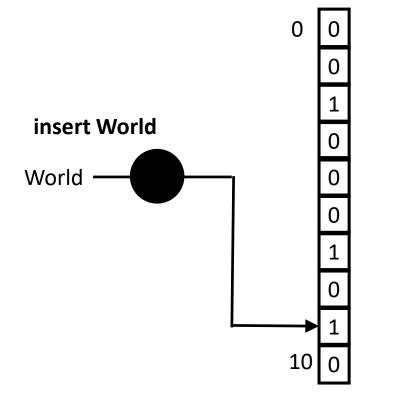
1-bit cells





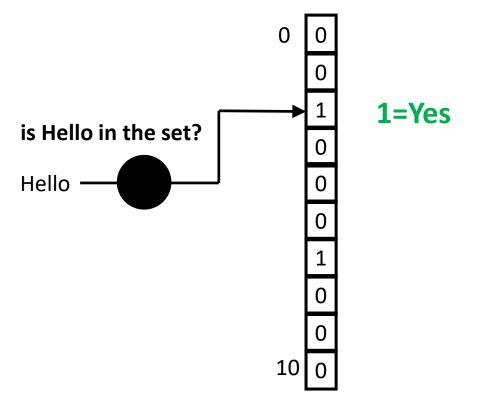
#### 1-bit cells





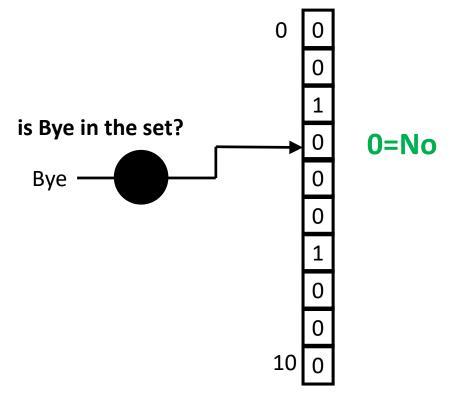
1-bit cells





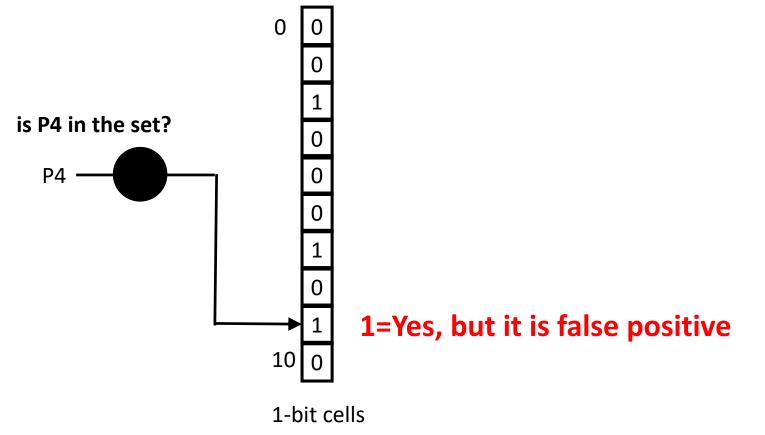
1-bit cells



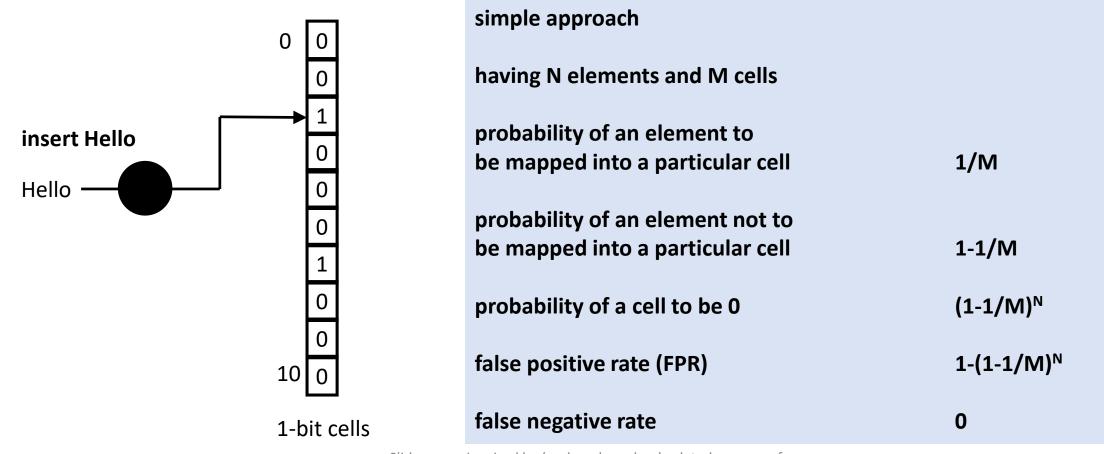


1-bit cells

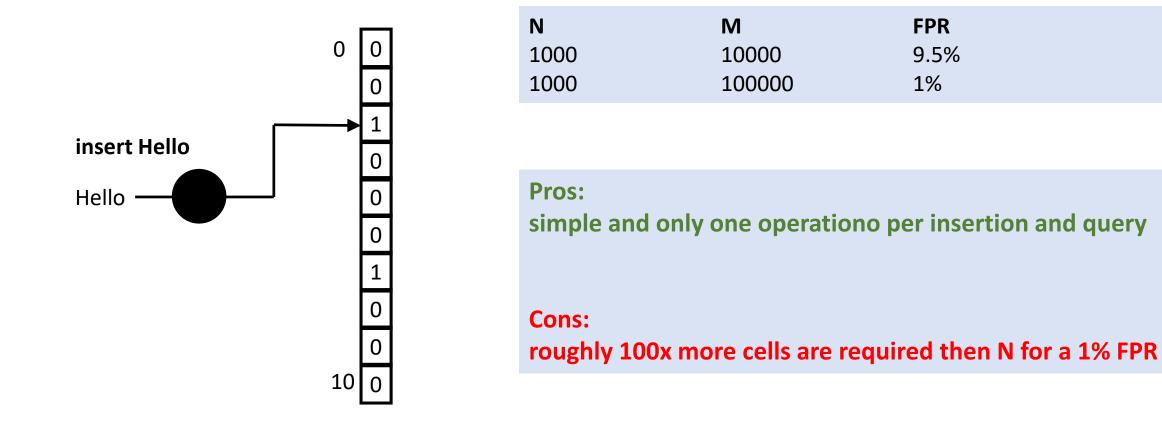








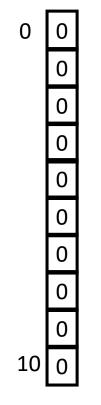




1-bit cells

How to implement a set 3rd approach – Bloom Filters

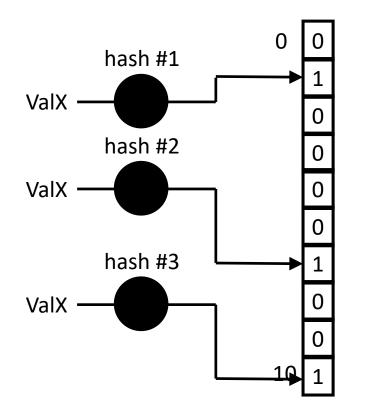




### 1-bit cells



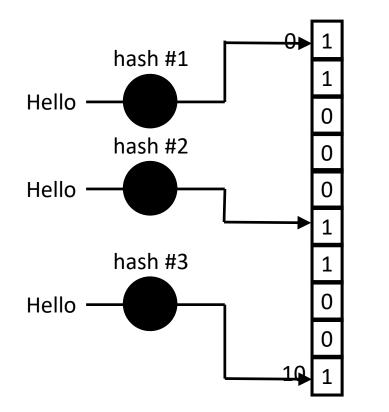
### insert ValX



1-bit cells



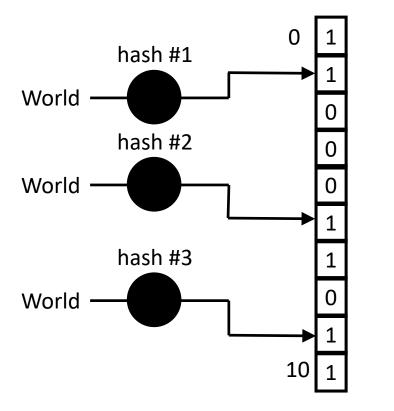
### insert Hello



1-bit cells



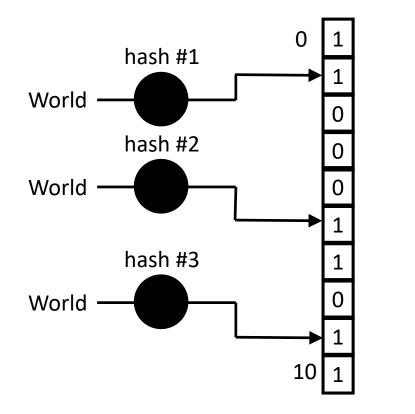
#### insert World



1-bit cells



### insert World



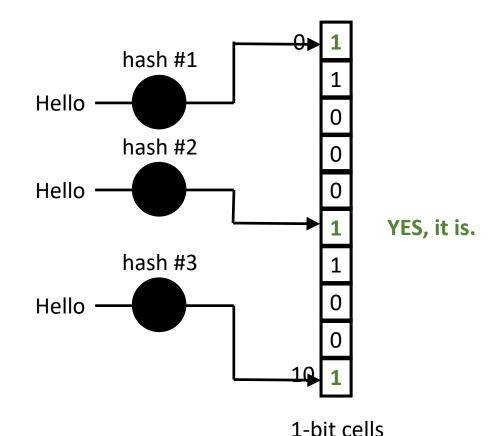
An element is considered in the set if **all** the hash values map **to a cell with 1** 

An element is not in the set if at least one hash value maps to a cell with 0

1-bit cells



### is Hello in the set?

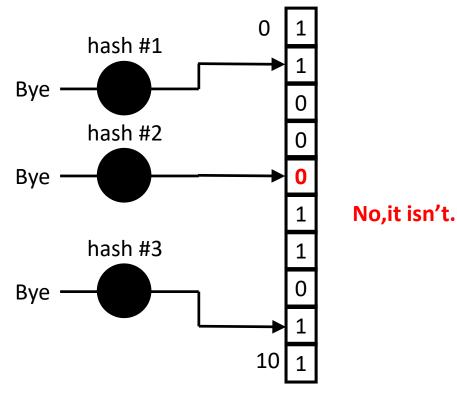


An element is considered in the set if **all** the hash values map **to a cell with 1** 

An element is not in the set if at least one hash value maps to a cell with 0



### Is Bye in the set?



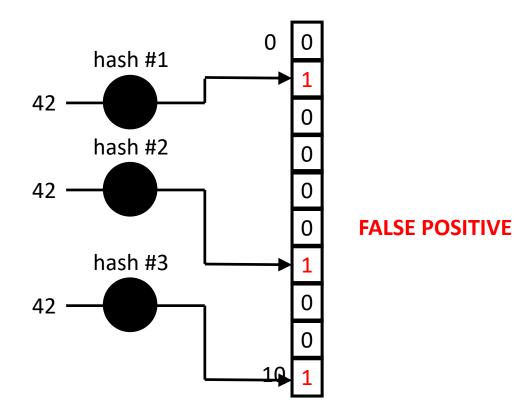
An element is considered in the set if **all** the hash values map **to a cell with 1** 

An element is not in the set if at least one hash value maps to a cell with 0

1-bit cells



### Is 42 in the set?



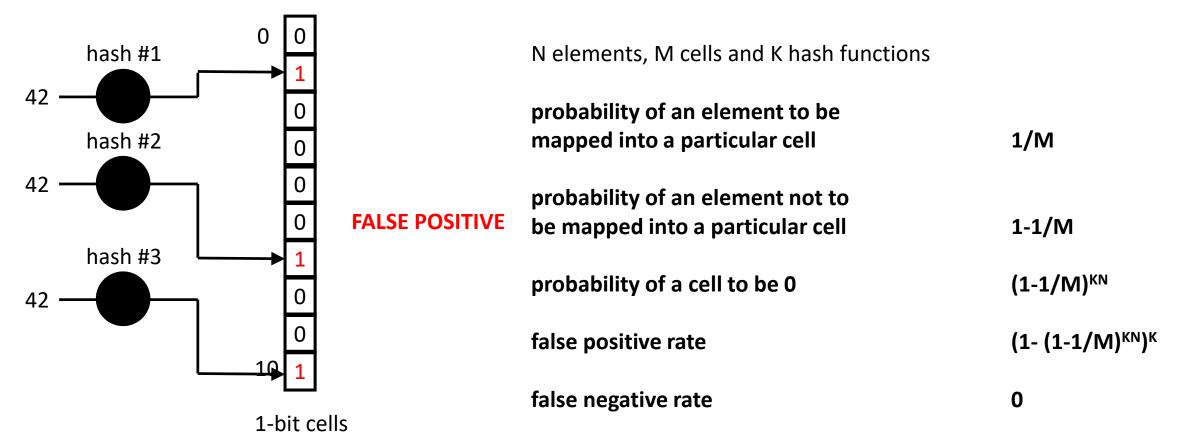
An element is considered in the set if **all** the hash values map **to a cell with 1** 

An element is not in the set if at least one hash value maps to a cell with 0

1-bit cells

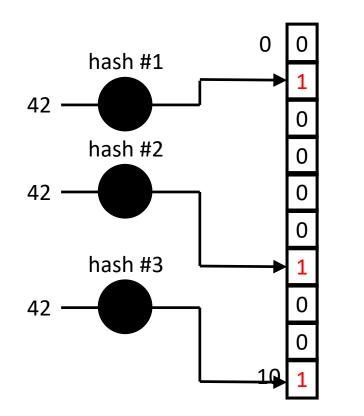


### Is 42 in the set?





### Is 42 in the set?



| Ν    | Μ      | К | FPR   |  |
|------|--------|---|-------|--|
| 1000 | 10000  | 7 | 0.82% |  |
| 1000 | 100000 | 7 | ~0%   |  |

#### **Pros:**

10x less memory usage than the simple approach

# Cons: slightly more operations required (e.g. 7 instead of 1)

1-bit cells

# Dimension your Bloom Filter



- N elements
- M cells
- K hash functions
- FP false positive rate

# Dimension your Bloom Filter



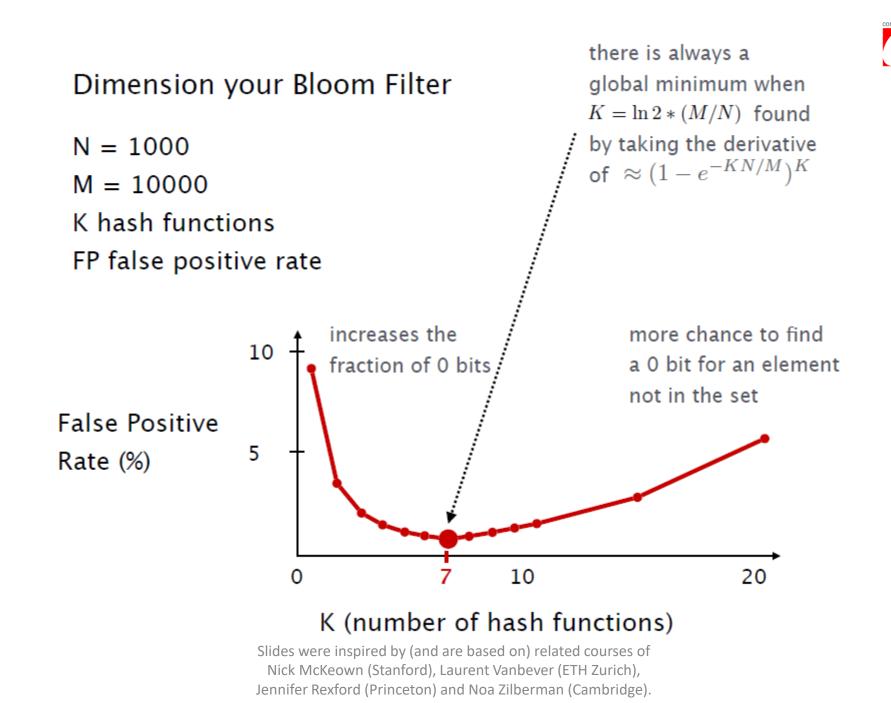
- N elements
- M cells
- K hash functions
- FP false positive rate

### asymptotic approx.

$$FP = (1 - (1 - \frac{1}{M})^{KN})^{K} \approx (1 - e^{-KN/M})^{K}$$

### with calculus you can

### dimension your bloom filter





### Implementation of a Bloom Filter in P416

### You will have to use hash functions

v1model

| enum HashAlgorithm { |
|----------------------|
| crc32,               |
| crc32_custom,        |
| crc16,               |
| s,                   |
| random,              |
| identity,            |
| csum16,              |
| xor16                |
| }                    |

extern register<T> {

register(bit<32> size);

void read(out T result, in bit<32> index); void write(in bit<32> index, in T value);

#### extern void hash<O, T, D, M>(out O result, in HashAlgorithm algo, in T base, in D data, in M max);

# Implementation in P4 with 2 hash functions

```
control MyIngress(...) {
```

```
register register <bit <1>> (NB_CELLS) bloom_filter;
```

```
apply {
    hash(meta.index1, HashAlgorithm.my_hash1, 0,
    {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);
    hash(meta.index2, HashAlgorithm.my_hash2, 0,
    {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);
    if (meta.to_insert == 1) {
        bloom_filter.write(meta.index1, 1);
        bloom_filter.write(meta.index2, 1);
    }
}
```

```
if (meta.to_query == 1) {
    bloom_filter.read(meta.query1, meta.index1);
    bloom_filter.read(meta.query2, meta.index2);
```

```
if (meta.query1 == 0 || meta.query2 == 0) {
    meta.is_stored = 0;
}
else {
```

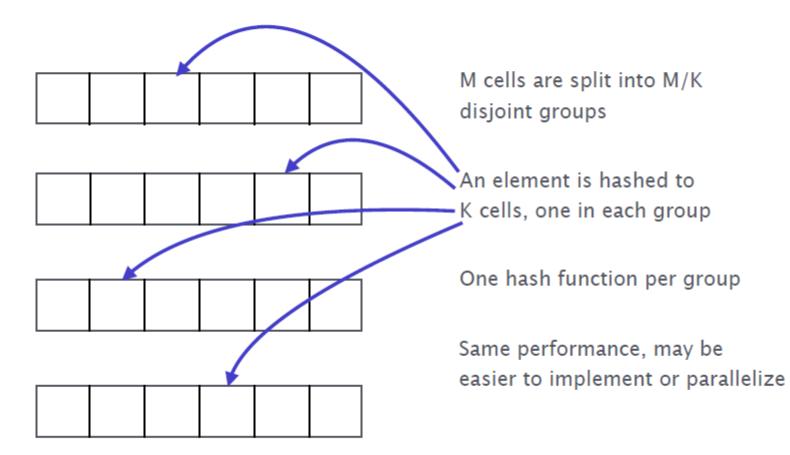
```
meta.is_stored = 1;
}
```

}}





# Depending on the hardware limitations, splitting the bloom filter might be required





# Because deletions are not possible, the **controller** may need to regularly **reset** the bloom filters

Resetting a bloom filter takes some time during which it is not usable

**Common trick:** use two bloom filters and use one when the controller resets the other one



# Why deletion is not easy?

# Solution



## **Counting Bloom Filters**